P27162.A04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Dureseti CHIDAMBARRAO, et al. Conf. No: 9755

Group Art Unit: 2812

Appln. No. : 10/667,601

Examiner: KENNEDY, Jennifer M.

Filed: September 23, 2003

For : IMPROVED NFETS USING GATE INDUCED STRESS MODULATION

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
U.S. Patent and Trademark Office
Customer Service Window, Mail Stop AF
Randolph Building
401 Dulany Street
Alexandria VA 22314

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § § 1.56 and 1.97-1.99, and supplemental to the Information Disclosure Statement filed on October 20, 2003, Applicants hereby call the following to the Examiner's attention.

The following documents were recently found by the Applicants:

US. Patent No.: 6,831,292 B2, issued December 14, 2004.

Further to the U.S. Patent and Trademark Office's decision to waive the requirement under 37 C.F.R. § 1.98 (a)(2)(i), copies of the U.S. patents and U.S. published patent applications are not enclosed herewith. However, if any copies are needed, the Examiner is respectfully requested to contact the undersigned.

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Applicants respectfully request that the Examiner consider the materials cited and indicate such consideration by appropriately initialing the enclosed PTO-1449 Form and including a copy of the initialed form in the next official communication.

Applicants note that this Information Disclosure Statement is being filed before receipt of the first action on the merits from the U.S. Patent and Trademark Office following the filing of an RCE.

Should there be any questions concerning this application, the Examiner is invited to contact the undersigned at the below listed telephone number.

Respectfully submitted,

Dureseti CHIDAMBARRAQ, et al.

Andrew M. Calderon Reg. No. 38,093

October 11, 2005 GREENBLUM & BERNSTEIN, P.L.C. 1950 Roland Clarke Place Reston, VA 20191 (703) 716-1191

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